**Fetch Decode Execute Cycle**

Fetch

The \_\_\_\_\_\_\_\_\_\_\_\_ gets the data from the \_\_\_\_\_\_\_\_\_\_\_\_ \_\_\_\_\_\_\_\_\_\_\_\_ and stores it in the registers ( the CPU’s memory).

Decode

Now that the CPU has the \_\_\_\_\_\_\_\_\_\_\_\_\_ it needs to understand the instruction to be able to \_\_\_\_\_\_\_\_\_\_\_ it.

Execute

Now the CPU can \_\_\_\_\_\_\_\_\_\_ the instruction. The CPU is designed to understand a set of \_\_\_\_\_\_\_\_\_\_\_\_.

Hard Disk, CPU, instruction, execute \*2, instructions.